

2/7

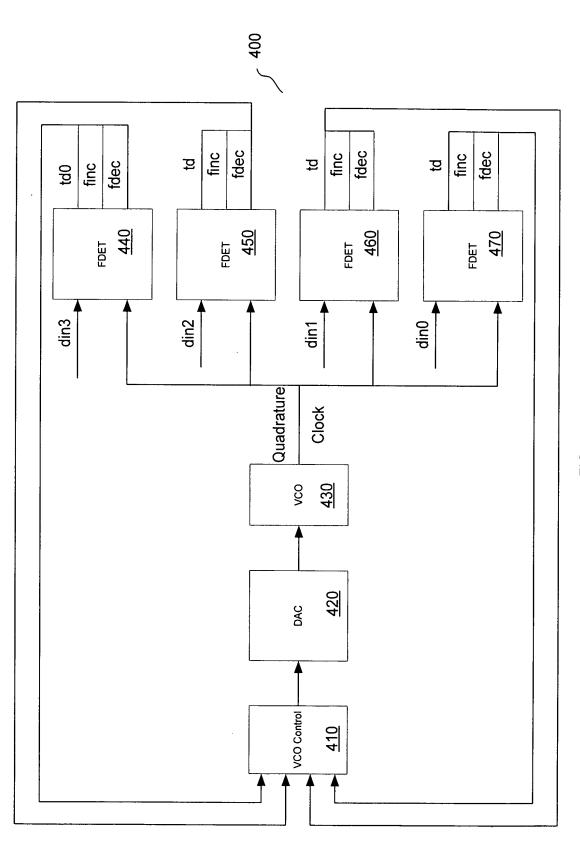


FIG. 4

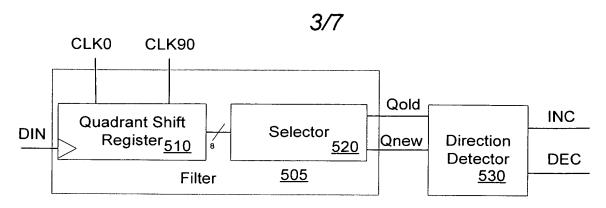


FIG. 5

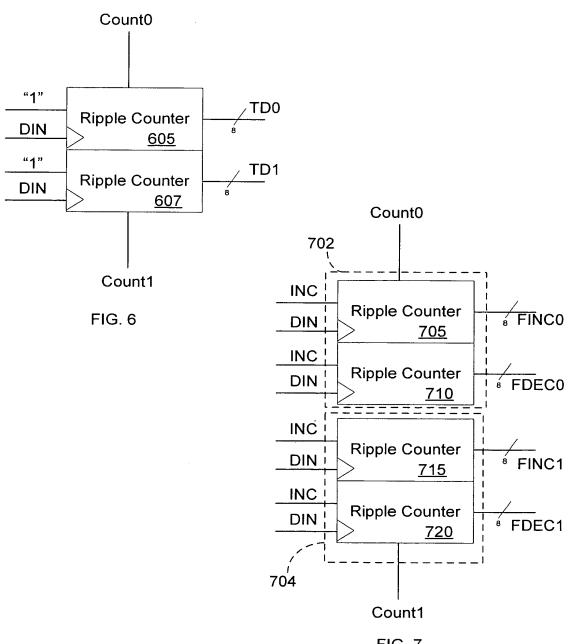


FIG. 7

4/7

Data Transition		Α	В	С	D	Е	F	G	Н	I	J	K	L
Quadrant		1	1	1	1	3	2	2	2	3	3	3	3
Filter Delay	1 Transition	X,	0	0	0	+/-	<u>-</u> 	0	0	+	0	0	0
	2 Transitions	х	X	Q	0	**	+	1/	0	, <b>+</b>	+	0	0
	3 Transitions	X	X	X	0	+/-	+	<b>4</b>	+	0	, . <del>, .</del>	+	+

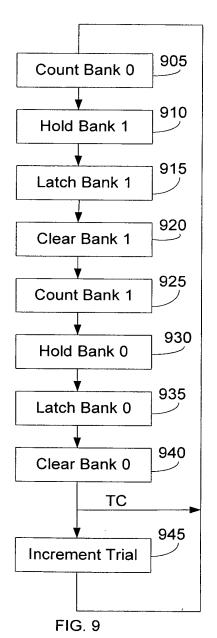


FIG. 8

Trial	Gain	Filter	Dither (%RMS)	Density Check	
1	1	1 delay	0.112	yes	
2	1/2	2 delays	0.028	yes	
3	1/4	4 delays	0.007	yes	
4	1/8	8 delays	none	yes	

FIG. 10

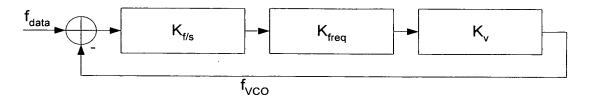
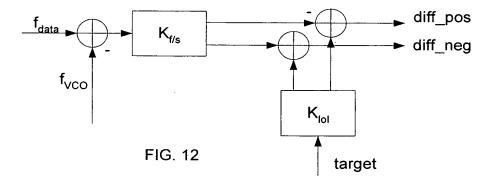
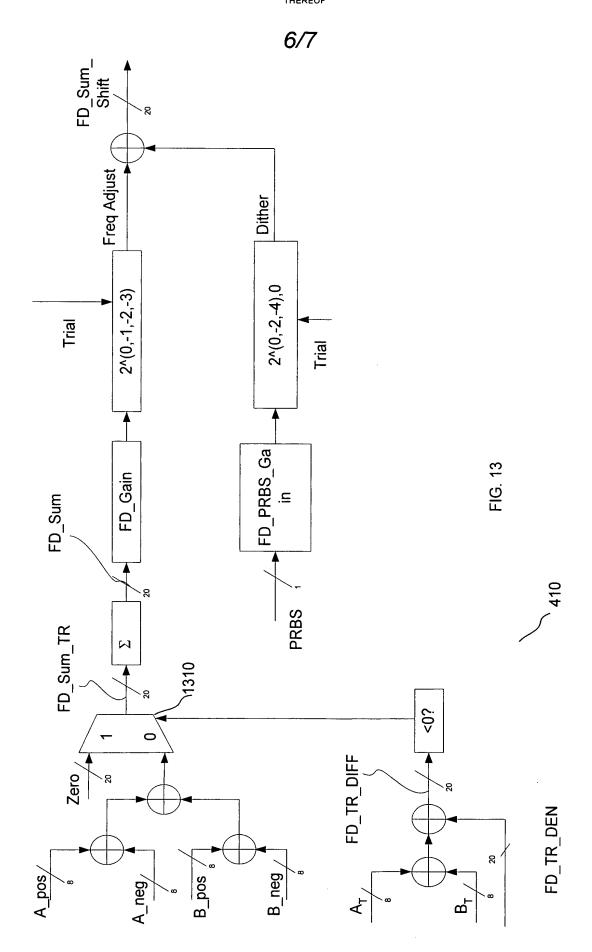


FIG. 11





1ST INVENTOR: ERIC KING
TITLE: CLOCK RECOVERY CIRCUIT AND METHOD
THEREOF

## 7/7

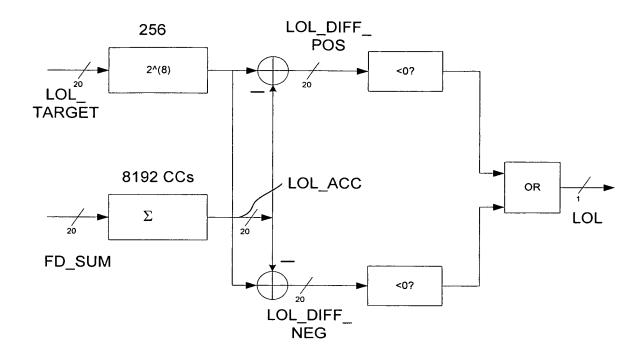


FIG. 14

.